
Subject: Supported DevicesPosted by [XcedentSupport](#) on Thu, 02 Jan 2014 04:08:01 GMT[View Forum Message](#) <> [Reply to Message](#)

The XCDT "Select" version supports the following ARM configurations:

Compiler Name	ARM Architecture	Example(s)
armv5te	armv5te	ARM946 with ARM-mode libraries
cortexm3	armv7	Cortex-M3
cortexm4	armv7	Cortex-M4
cortexm4vfp16 floating-point linkage	armv7	Cortex-M4 with Single-Precision, 16 Register VFP and "hard"
thumb5e	armv5te	ARM946 with Thumb-mode libraries

The XCDT "Full" version supports the following ARM configurations:

Compiler Name	ARM Architecture	Example(s)
armv4t	armv4t	arm7tdmi with ARM-mode libraries
armv4tcpu linkage	armv4t	arm7tdmi with ARM-mode libraries and "hard" FPU floating-point linkage
armv5t	armv5t	arm9 with ARM-mode libraries
armv5t	armv5t	arm9 with ARM-mode libraries and "hard" FPU floating-point linkage
armv5te	armv5te	ARM946 with ARM-mode libraries
armv5tecpu linkage	armv5te	ARM946 with ARM-mode libraries and "hard" FPU floating-point linkage
cortexa5	armv7	Cortex-A5
cortexa5neon floating-point linkage	armv7	Cortex-A5 with NEON floating-point processor and "hard" floating-point linkage
cortexa7	armv7	Cortex-A7
cortexa7neon floating-point linkage	armv7	Cortex-A7 with NEON floating-point processor and "hard" floating-point linkage
cortexa8neon floating-point linkage	armv7	Cortex-A8 with NEON floating-point processor and "hard" floating-point linkage
cortexa9neon floating-point linkage	armv7	Cortex-A9 with NEON floating-point processor and "hard" floating-point linkage
cortexa9vfp linkage	armv7	Cortex-A9 with VFP floating-point processor and "hard" floating-point linkage
cortexa15neon floating-point linkage	armv7	Cortex-A15 with NEON floating-point processor and "hard" floating-point linkage
cortexm0	armv6-m	Cortex-M0
cortexm1	armv6-m	Cortex-M1
cortexm3	armv7	Cortex-M3
cortexm4	armv7	Cortex-M4
cortexm4vfp	armv7	Cortex-M4 with VFP floating-point processor and "hard" floating-point linkage

linkage		
cortexm4vfp16	armv7	Cortex-M4 with Single-Precision, 16 Register VFP and "hard" floating-point linkage
cortexr4	armv7	Cortex-R4
cortexr4vfp	armv7	Cortex-R4 with VFP floating-point processor and "hard" floating-point linkage
cortexr5vfp	armv7	Cortex-R5 with VFP floating-point processor and "hard" floating-point linkage
thumb4	armv4t	Generic ARM v4 with Thumb-mode libraries
thumb5	armv5t	Generic ARM v5 with Thumb-mode libraries
thumb5e	armv5te	ARM946 with Thumb-mode libraries